Right-to-Left Code Generation for Arithmetic Expressions

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This paper presents a method of right-to-left code generation for arithmetic expressions given in the postfix notation. The number of generated instruction lines is equal to the one obtained from the binary tree structure. However, the right-to-left code generator is faster and requires less memory than the code generator from the binary tree. These results were obtained by introducing a vector, called vector-generator, assigned to every postfix string. A translation grammar which generates the postfix string and its associated vector-generator is defined. An experiment has been performed over a set of arithmetic expressions to compare the right-to-left code generator and the code generator from the binary tree.

INTRODUCTION

In this paper, we shall concentrate on the design of an efficient code generator which produces an assembly language code for arithmetic expressions. The input to the code generator is an arithmetic expression. The output is an equivalent assembly language program. We would like the resulting assembly language program to be good under some cost function such as the number of assembly language instructions, or the number of memory fetches. The cost function of the code generator depends only on the internal representation of expressions in compilers. The well-known internal structures are parenthesized notation, quadruples, triples, postfix and prefix notation, and the binary tree. The parenthesized form of expressions was used in the first FORTRAN compilers. This is not a suitable internal structure for an efficient code generation. In particular, the transformation to such a structure is not compatible with the design of modern compilers, as far as a single processor is concerned. Stone gives the one-pass algorithm which transforms the expressions into a parenthesized form for parallel processors. In quadruple notation, the operations appear in the order in which they are to be executed. The main problem is to keep track of the contents of the accumulator, in which all arithmetic is done at run-time, in order to eliminate unnecessary load and store instructions. The main disadvantage of quadruples is that a description of each temporary value is maintained throughout the compile-time. This problem does not exist in the triple notation first introduced by Sheridan. We do not need a description of each temporary when using triples; it need only be maintained while code which references it is being generated. If the ranges of temporaries are pairwise disjoint or nested, we can use a compile-time stack to hold descriptions of temporaries; otherwise, a more complex scheme will be necessary to allocate and delete space for them. If the triples are not going to be processed once the code has been generated, we can replace the triple by a description of its result. Consequently, we would not need a stack, and we would not have to delete descriptions. Obviously, the handling of temporaries in triple notation is more complex than in the quadruple notation. Thus, the code generator from the quadruple notation is more efficient. On the other hand, the memory space for triples is smaller since they require three fields per entry. The triples are more suitable for the code optimization since they can easily be rearranged for this purpose. Furthermore, the code generated from the triple notation generally uses fewer temporaries.

A number of languages are based on a concept known as a Polish notation; this has advantages for machine but it is difficult for human digestion. It was first introduced by the Polish philosopher Jan Lukasiewicz in connection with the formulae of symbolic logic. A variation of it is called the reverse Polish notation. These notations are also known as the prefix and postfix notation, respectively. The transformation to prefix notation, and the code generation from it, requires the analysis of expressions from right to left. This differs from the previously mentioned methods where the analysis is performed from left to right. In postfix notation, the analysis is also performed from left to right. Formally, the difference between postfix and prefix notation lies in the operator structure; reverse Polish notation is based on postfix operators—<operator> <operand> <operand>—while direct Polish notation is based in a similar way on prefix operators—<operator> <operand> <operand>. Since the writing and interpretation of expressions are usually performed from left to right, the postfix notation is inappropriate for the code generation (with the exception of APL, for example). Compared to the triple notation, the postfix notation requires less space and generates fewer temporaries; the code generation from it is very simple. However, the postfix notation is not a structure particularly suitable for code optimization either at the single expression level or at the level of a group of expressions.

Code generation from a binary tree is probably the most discussed method in this field. Anderson has described an algorithm for the code generation for a one-register machine. Nakata proposed a similar algorithm. The number of registers required to generate code for an expression tree has been investigated by Nakata, Redziejowski, and Sethi and Ullman. Beatty and Fraley discuss extensions involving the unary minus operator. The code generated from the binary tree is the shortest-length program to compute a given expression.
This was shown by Aho and Ullman.\textsuperscript{12} Besides the shortest-length program, the binary tree uses fewer temporaries and offers a more suitable structure for the straight-line code optimization than the postfix notation. On the other hand, the postfix notation requires less space and is more efficient from the transformation and code generation point of view.

There are other works dealing with the problem of register allocation in a sequence of expression evaluations (e.g., Refs 13–15), translating arithmetic expressions into code for parallel computers (e.g., Refs 2, 16–19), or basic algorithms for the code generation.\textsuperscript{20–23} This paper presents a new algorithm for the code generation without optimization. The work was motivated by the results of the investigation of the distribution of executable statements,\textsuperscript{24} and by the examination of works concerning the code optimization. It was found that the assignment and IF statements together have over 60\% of static distribution.\textsuperscript{24} Furthermore, the existing internal structures of expressions are not particularly suitable for the code optimization and register allocation at the single expression level or at the level of a group of expressions. With respect to other algorithms in this field, the proposed algorithm gives faster code generation than the binary tree but for the same length of the code. Nevertheless, the internal structure of expressions is suitable for the code optimization and register allocation. It offers an opportunity to investigate the efficient code optimization techniques.

The internal structure of an expression is composed of two substructures: (1) well-known postfix notation, and (2) vector-generatrice (VG), describing the postfix notation in a certain way. Its generation is discussed through a translation grammar for arithmetic expressions. Although the transformation to such an internal structure is performed from left to right, the code is generated from right to left. The algorithm is presented in Algol-like form and discussed. It is proved that the algorithm produces the code of the same length as the binary tree. All considerations are based on a single accumulator machine.

Theoretical results are illustrated by a worked and output example. The output example is extracted from an experiment done for a set of arithmetic expressions to examine the characteristics of the algorithm and compare them to that obtained with the binary tree. It was found that the proposed algorithm is about 20\% faster. When the transformation to internal structure is taken into account, this time gain reduces to 13\%. The required memory space and number of instructions necessary to realize the transformation and code generation are approximately the same.

**VECTOR-GENERATRICE**

In the generation of VG-based data structure we shall use the concept of the translation grammar.\textsuperscript{25} A translation grammar is a context-free grammar in which the set of terminal symbols is partitioned into a set of input symbols and a set of action symbols. The action symbols are used to represent more general ad hoc routines. The strings in the language specified by a translation grammar are called activity sequences.

Let a translation grammar $G^T$ be given generally by

\[
G^T = (IS, AS, NS, P, SN)
\]

where $IS = \text{input symbols}, AS = \text{action symbols}, NS =$ nonterminal symbols, $P =$ productions and $SN =$ starting nonterminal.

The following metanotation is used for $G^T$:

\[
\langle, \rangle = \text{matched pair of angle brackets enclosing a nonterminal}, \quad \\
\rightarrow = \text{production operator}, \quad \\
\{, \} = \text{matched pair of curly brackets enclosing an action symbol}
\]

An example of a translation grammar is:

\[
\begin{align*}
IS &= \{a, b, c\} \\
AS &= \{x, y, z\} \\
NS &= \langle A \rangle, \langle B \rangle \\
SN &= \langle A \rangle \\
(1) P &= \langle A \rangle \rightarrow a\langle A \rangle\{x\}\langle B \rangle \\
(2) &= \langle A \rangle \rightarrow \{x\} \\
(3) &= \langle B \rangle \rightarrow \langle B \rangle\{c\} \\
(4) &= \langle B \rangle \rightarrow \langle y \rangle \\
\end{align*}
\]

For the input sequence $abc$, the activity sequence is:

\[
a\{z\}\{x\}\langle y \rangle\{c\}.
\]

It is obtained by the following parsing:

\[
\begin{align*}
(1) &= \langle A \rangle \rightarrow a\langle A \rangle\{x\}\langle B \rangle \\
(2) &= \langle A \rangle \rightarrow \{x\} \\
(3) &= \langle A \rangle \rightarrow \langle A \rangle\{x\}\langle B \rangle\{c\} \\
(4) &= \langle A \rangle \rightarrow \langle A \rangle\{x\}\langle y \rangle\{c\}
\end{align*}
\]

If each action symbol represents a routine which outputs the symbol within the curly brackets (and possibly performs anything else), then a given translation grammar is intended to describe the translation of input strings into output strings. For the given example, the output string is $zyx$. It is readily derived from the obtained activity sequence. This concept of the translation grammar will be used in the derivation of VG-based data structure.

As the first step toward this goal, we devise a simplified translation grammar for arithmetic expressions:

\[
\begin{align*}
IS &= I, N, +, -, *, /, $ \\
AS &= I, N, +, -, #, *, /, $ (\_, denotes the empty symbol) \\
NS &= \langle S \rangle = \text{(start expression)} \\
SN &= \langle N \rangle = \text{(unsigned symbol)} \\
\end{align*}
\]

\[
\begin{align*}
(1) P &= \langle S \rangle \rightarrow \{ \langle E \rangle \} \{ \{ S \} \} \\
(2) &= \langle E \rangle \rightarrow \langle E \rangle + \langle T \rangle \{ + \} \\
(3) &= \langle E \rangle \rightarrow \langle E \rangle - \langle T \rangle \{ - \} \\
(4) &= \langle E \rangle \rightarrow \langle T \rangle \\
(5) &= \langle T \rangle \rightarrow \langle T \rangle \ast \langle P \rangle \{ \ast \} \\
(6) &= \langle T \rangle \rightarrow \langle T \rangle / \langle P \rangle \{ / \} \\
(7) &= \langle T \rangle \rightarrow \langle P \rangle \\
(8) &= \langle P \rangle \rightarrow + \langle P \rangle \\
(9) &= \langle P \rangle \rightarrow - \langle P \rangle \{ \# \} \\
(10) &= \langle P \rangle \rightarrow ( \langle V \rangle \{ \langle I \rangle \} \\
(11) &= \langle P \rangle \rightarrow \langle N \rangle \{ \langle N \} \\
(12) &= \langle P \rangle \rightarrow ( \langle E \rangle \{ \langle E \} \\
(13) &= \langle V \rangle \rightarrow I \\
(14) &= \langle N \rangle \rightarrow N
\end{align*}
\]
It should be noted that the unary operators + and # (unary minus) are given the highest precedence. Each operator can be followed by a string of unary operators.

The input sequences are restricted to symbols $I$ (representing a variable), $N$ (representing an unsigned number), (,) and +, −, *, / only for the sake of the clear and concise presentation of the algorithm. The function designator and the operator ⌫ could be readily included in the grammar. Their presence or absence does not influence the final conclusion on the algorithm characteristics.

If we assign to each action symbol an action routine for performing special functions, then the application of the translation grammar given by Eqn (2) to an input sequence will produce a standard postfix string with associated VG. There is a very simple practical explanation of VG. Let $E_p$ be an arithmetical expression in the infix notation, transformed to postfix string $E_p$:

$$E_p = a_1 a_2 \ldots a_m b_1 b_2 \ldots b_l a_{m+1} a_{m+2} \ldots a_{m+i} b_{j+1} b_{j+2} \ldots b_{j+k} a_{m+i+1} a_{m+i+2} \ldots a_{m+i+M+i} a_{m+i+M+i} \ldots a_{m+i+M+i+n+1} \ldots b_{j+k+1} a_{m+i+M+i+n+1} \ldots b_{j+k+n} a_{m+i+M+i+n+1} \ldots$$

where $a_1$, $a_2$, $a_3$, $a_m$, $b_1$, $b_2$, $b_3$, $b_l$, $b_{j+1}$, $b_{j+2}$, $b_{j+k}$, $b_{j+k+1}$ are the operands (or their addresses), and $b_{j+1}$, $b_{j+2}$, $b_{j+k}$, $b_{j+k+1}$ are the operators (or their indices in delimiter table). The dollar sign $\$ $ denotes the end of $E_p$, i.e.

$$a_{m+i+M+i+1} = \$ \quad (4)$$

Using Eqn (3), VG can be generated in the following way. Firstly, we define the substrings of $E_p$. A substring is a sequence of all successive operands or operators. If the number of operand substrings is $N_1$, the number of operator substrings $N_2$ must be

$$N_3 = 2N_1 + 1 \quad (6)$$

By assigning to each substring of operands a successor substring of operators, one obtains a pair of connected substrings $CS_1$, where $I$ is its ordinal number.

For example, $a_1 a_2 \ldots a_m b_1 b_2 \ldots b_l$ forms a pair of connected substrings $CS_1 = a_1 a_2 \ldots a_m$ and $CS_2 = b_1 b_2 \ldots b_l$ represents a substring of operands followed by a successor substring of operators $b_1 b_2 \ldots b_l$. The number of pairs $N_{CS}$ is

$$N_{CS} = (\lfloor N_3/2 \rfloor - 1) \quad (7)$$

For each pair of connected substrings we create a 3-tuple $(N_{1t}, N_{1b}, P_l)$, where $N_{1t}$ = number of operands in $CS_1$, $N_{1b}$ = number of operators in $CS_1$, $P_l$ = pointer to beginning of $CS_1$ in $E_p$. For $CS_1$, the 3-tuple is $(m, j, 1)$ because $N_{1t} = m$, $N_{1b} = j$ and $P_l = 1$. VG is formed now by concatenating all 3-tuples of connected substrings by their ordinal numbers into a vector of dimension $(N_{CS} + 1) \times 3$:

$$V_{G1} catV_{G2} cat \ldots catV_{G_{N_{CS}+1}} \quad (8)$$

where $cat$ is the concatenation operator. The endmarker is considered as an operand substring connected to a dummy operator substring. Its corresponding 3-tuple is given by $(1, 0, P_{N_{CS}+1})$.

**ACTION ROUTINES**

In generating VG-based data structure, the action routines play the principal role. In order to describe them, we associate with each action symbol a mnemonic name representing a routine intended to perform a particular function:

<table>
<thead>
<tr>
<th>Action symbol</th>
<th>Name of routine</th>
</tr>
</thead>
<tbody>
<tr>
<td>/</td>
<td>VARIABLE</td>
</tr>
<tr>
<td>+</td>
<td>UNSIGNED_NUMBER</td>
</tr>
<tr>
<td>−</td>
<td>MINUS</td>
</tr>
<tr>
<td>*</td>
<td>UNARY_MINUS</td>
</tr>
<tr>
<td>#</td>
<td>MULTIPLY</td>
</tr>
<tr>
<td>$</td>
<td>DIVIDE</td>
</tr>
<tr>
<td>END-MARKER</td>
<td></td>
</tr>
</tbody>
</table>

We shall proceed now with the presentation of these routines as Algol procedures.

**NULL**

```algol
procedure NULL(VG, current_VG_index, current_index_of_output_string);
  integer array VG;
  integer current_VG_index, current_index_of_output_string;
  procedure NULL(VG, current_VG_index, current_index_of_output_string);
  integer n, current_VG_index, current_index_of_output_string;
  begin
    comment: because the empty symbol is to be outputted, no output operation is performed;
    current_VG_index := 1;
    current_index_of_output_string := 1;
    comment: reset VG array;
    for i := 1 step 1 until n do
      if VG[i] = 0 then
        VG[i] := 1;
        VG[3] := 1;
      end NULL;
  end NULL;
```

The routine NULL resets the space allocated to VG (defined as a vector), and initializes the indices of the VG and output string array ($E_p$).

**VARIABLE**

```algol
procedure VARIABLE(VG, current_VG_index, class_of_previously_outputted_symbol, output_string, current_index_of_output_string);
  integer array VG; string array output_string; integer current_VG_index, current_index_of_output_string;
  procedure VARIABLE(VG, current_VG_index, class_of_previously_outputted_symbol, output_string, current_index_of_output_string);
  begin
    if current_VG_index ≠ 1 then
      comment: output the action symbol $I$ into the output string array;
      current_index_of_output_string := current_index_of_output_string + 1;
      output_string[current_index_of_output_string] := "V";
      comment: count the number of outputted symbols of the class 0;
      VG[current_VG_index] := VG[current_VG_index] + 1;
      class_of_previously_outputted_symbol := 0;
      end VARIABLE;
```

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The routine VARIABLE performs three functions: (1) compares the class of the previously outputted symbol to the current one (OR N) for current_VG_index ≠ 1; the class of outputted symbol is class_of (I) = 0, class_of (N) = 0, class_of (other action symbols) = 1; (2) outputs the action symbol I into the output string array (defined as a vector); (3) counts the number of outputted symbols of the class 0.

UNSIGNED NUMBER

The body of this procedure is identical to VARIABLE except for the statement

`output_string [current_index_of_output_string] = '1'`;

which has to be replaced by

`output_string [current_index_of_output_string] = 'N';`

PLUS

procedure PLUS (VG, current_VG_index, output_string, current_index_of_output_string, class_of_previously_outputted_symbol);

integer array VG; string array output_string; integer current_VG_index, output_string [current_index_of_output_string];

begin
comment: output the action symbol + into the output string array;
output_string [current_index_of_output_string] = '+';
comment: count the number of outputted symbols of the class 1;
VG [current_VG_index + 1] = VG [current_VG_index + 1] + 1;
class_of_previously_outputted_symbol = 1;
end PLUS;

The routine PLUS performs two functions: (1) outputs the action symbol + into the output string array, and (2) counts the number of outputted symbols of the class 1.

MINUS

The body of this procedure is identical to PLUS except for the statement

`output_string [current_index_of_output_string] = '1';`

which has to be replaced by

`output_string [current_index_of_output_string] = 'N';`

UNARY MINUS

The body of this procedure is identical to PLUS except for the statement

`output_string [current_index_of_output_string] = '1';`

which has to be replaced by

`output_string [current_index_of_output_string] = 'N';`

MULTIPLY

The body of this procedure is identical to PLUS except for the statement

`output_string [current_index_of_output_string] = '1';`

which has to be replaced by

`output_string [current_index_of_output_string] = 'N';`

DIVIDE

The body of this procedure is identical to PLUS except for the statement

`output_string [current_index_of_output_string] = '1';`

which has to be replaced by

`output_string [current_index_of_output_string] = 'N';`

END MARKER

procedure END_MARKER (VG, current_VG_index, output_string, current_index_of_output_string);

integer array VG; string array output_string; integer current_VG_index, output_string [current_index_of_output_string];

begin
comment: output the action symbol $ into the output string array;
output_string [current_index_of_output_string] = '$';
comment: terminate VG by a special three words marker;
current_VG_index = current_VG_index + 3;
VG [current_VG_index] = 1;
VG [current_VG_index + 1] = 0;
VG [current_VG_index + 2] = VG [current_VG_index - 1] +
+ VG [current_VG_index - 2] +
+ VG [current_VG_index - 3];
end END_MARKER;

It is obvious that the procedures VARIABLE, UNSIGNED NUMBER, and the procedures PLUS, MINUS, UNARY_MINUS, MULTIPLY, DIVIDE can be replaced (in the domain of realization) by two procedures: (1) OPERAND, and (2) OPERATOR. This is obtained easily by including the output symbol in the list of formal parameters.

OPERAND

procedure OPERAND (VG, current_VG_index, class_of_previously_outputted_symbol, output_string, current_index_of_output_string, output_symbol);

integer array VG; string array output_string; integer current_VG_index, class_of_previously_outputted_symbol, current_index_of_output_string;

begin
string (1) output_symbol;
begin
if current_VG_index ≠ 1 \class_of_previously_outputted_symbol ≠ 0

then

begin
VG [current_VG_index + 1] = VG [current_VG_index] +
+ VG [current_VG_index + 1] +
+ VG [current_VG_index + 2];
current_VG_index = current_VG_index + 3;
end;

comment: output the action symbol (output_string) into the output string array;
output_string [current_index_of_output_string] = output_symbol;
current_index_of_output_string = current_index_of_output_string;

end OPERAND;

The procedure OPERAND replaces the procedures VARIABLE and UNSIGNED NUMBER.

OPERATOR

procedure OPERATOR (VG, current_VG_index, output_string, current_index_of_output_string, output_symbol, class_of_previously_outputted_symbol);

integer array VG; string array output_string; integer current_VG_index, output_string [current_index_of_output_string], class_of_previously_outputted_symbol;
The procedure OPERATOR replaces the procedures PLUS, MINUS, UNARY_MINUS, MULTIPLY, and DIVIDE.

As an illustration, consider the following arithmetic expression

\[ I + N - (-(N + I))(I * I - N/I))I * I + J \]  

(9)

whose VG-based data structure is to be found. The parsing sequence is:

(1) \{E\} $\langle E \rangle$
(2) \{E\} \{T\} \{+\} \{E\} \{P\}
(3) \{E\} \{T\} \{-\} \{E\} \{P\}
(4) \{E\} \{T\} \{+\} \{E\} \{P\}
(5) \{E\} \{T\} \{+\} \{E\} \{P\}
(6) \{E\} \{T\} \{+\} \{E\} \{P\}
(7) \{E\} \{T\} \{+\} \{E\} \{P\}
(8) \{E\} \{T\} \{+\} \{E\} \{P\}
(9) \{E\} \{T\} \{+\} \{E\} \{P\}
(10) \{E\} \{T\} \{+\} \{E\} \{P\}
(11) \{E\} \{T\} \{+\} \{E\} \{P\}
(12) \{E\} \{T\} \{+\} \{E\} \{P\}

The term right-to-left code generation designates the code generation from the right end of \( E_p \). One of the key points in introducing the vector-generatrice is just to enable the right-to-left code generation. The algorithm is based on the following principles: (i) the elementary unit for the code generation is a pair of connected substrings \( C_S \); (ii) the code generation from an elementary unit is the same as for any standard postfix string; (iii) the code is generated until \( TV,0 = 1 \) \( v \) \( A^B = 0 \); (iv) when the current \( C_S \) becomes temporary inactive, i.e. \( NaaS = 1 \) \( v \) \( NaaS = 0 \), the next \( C_S \) is taken, where \( I' = I + 1 \) \( v \) \( I' = I' - 1 \) respectively; (v) the separate stack for operands is not needed; instead, the space for the code generation is a pair of connected substrings \( C_S \). Each underlined 3-tuple in VG corresponds to a \( C_S \) in \( E_p \) by the order of its appearance.

Right-to-left code generation

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**RIGHT-TO-LEFT CODE GENERATION**

The term right-to-left code generation designates the code generation from the right end of \( E_p \). One of the key points in introducing the vector-generatrice is just to enable the right-to-left code generation. The algorithm is based on the following principles: (i) the elementary unit for the code generation is a pair of connected substrings \( C_S \); (ii) the code generation from an elementary unit is the same as for any standard postfix string; (iii) the code is generated until \( TV,0 = 1 \) \( v \) \( A^B = 0 \); (iv) when the current \( C_S \) becomes temporary inactive, i.e. \( NaaS = 1 \) \( v \) \( NaaS = 0 \), the next \( C_S \) is taken, where \( I' = I + 1 \) \( v \) \( I' = I' - 1 \) respectively; (v) the separate stack for operands is not needed; instead, the space for the code generation is a pair of connected substrings \( C_S \). Each underlined 3-tuple in VG corresponds to a \( C_S \) in \( E_p \) by the order of its appearance.

Before we proceed to detailed presentation of algorithm, we shall explain the procedure for the code generation from an elementary unit \( C_S \).

Firstly, we shall define a hypothetical machine, necessary for this purpose, as a single accumulator (ACC) machine with the following basic operations:

- **LDA**: move the contents of the location \( A \) into ACC,
- **STA**: move the contents of ACC into the location \( A \),
- **NEG**: the contents of ACC is two's complemented,
- **ADD**: the contents of the location \( A \) are added to the contents of ACC and the result placed in ACC,
- **SUB**: the contents of ACC are subtracted from the contents of \( A \) and the result placed in ACC,
- **MUL**: the contents of ACC are multiplied by the
Table 1. Generation of VG-based data structure for expression $1 + N - (- (N + I)/(I*I - N/I))/I*I + 1$

<table>
<thead>
<tr>
<th>ENTRY</th>
<th>ACTIVITY</th>
<th>OUTPUT_STRING</th>
<th>Current_index_of_output_string</th>
<th>VG</th>
<th>Class_of_previously_outputted_symbol</th>
</tr>
</thead>
<tbody>
<tr>
<td>[ ]</td>
<td>NULL</td>
<td></td>
<td>1</td>
<td>0</td>
<td>-</td>
</tr>
<tr>
<td>[l]</td>
<td>VARIABLE</td>
<td>I</td>
<td>2</td>
<td>10</td>
<td>0</td>
</tr>
<tr>
<td>[N]</td>
<td>UNSIGNED_NUMBER</td>
<td>IN</td>
<td>3</td>
<td>20</td>
<td>0</td>
</tr>
<tr>
<td>[+ ]</td>
<td>PLUS</td>
<td>IN +</td>
<td>4</td>
<td>21</td>
<td>0</td>
</tr>
<tr>
<td>[N]</td>
<td>UNSIGNED_NUMBER</td>
<td>IN + N</td>
<td>5</td>
<td>211</td>
<td>0</td>
</tr>
<tr>
<td>[l]</td>
<td>VARIABLE</td>
<td>IN + NI</td>
<td>6</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[+ ]</td>
<td>PLUS</td>
<td>IN + NI +</td>
<td>7</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[g]</td>
<td>UNARY_MINUS</td>
<td>IN + NI + #</td>
<td>8</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[l]</td>
<td>VARIABLE</td>
<td>IN + NI + #i</td>
<td>9</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[l]</td>
<td>VARIABLE</td>
<td>IN + NI + #i</td>
<td>10</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[l]</td>
<td>VARIABLE</td>
<td>IN + NI + #i</td>
<td>11</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[l]</td>
<td>VARIABLE</td>
<td>IN + NI + #i</td>
<td>12</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[N]</td>
<td>UNSIGNED_NUMBER</td>
<td>IN + NI + #i*</td>
<td>13</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[l]</td>
<td>VARIABLE</td>
<td>IN + NI + #i 14</td>
<td>14</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>[/]</td>
<td>DIVIDE</td>
<td>IN + NI + #i* NI /</td>
<td>15</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[- ]</td>
<td>MINUS</td>
<td>IN + NI + #i* NI /</td>
<td>16</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[/]</td>
<td>DIVIDE</td>
<td>IN + NI + #i* NI /</td>
<td>17</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[l]</td>
<td>VARIABLE</td>
<td>IN + NI + #i* NI /</td>
<td>18</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[/]</td>
<td>DIVIDE</td>
<td>IN + NI + #i* NI /</td>
<td>19</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[' ']</td>
<td>MULTIPY</td>
<td>IN + NI + #i* NI /</td>
<td>20</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[- ]</td>
<td>MINUS</td>
<td>IN + NI + #i* NI /</td>
<td>21</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[' ']</td>
<td>MULTIPY</td>
<td>IN + NI + #i* NI /</td>
<td>22</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[+ ]</td>
<td>PLUS</td>
<td>IN + NI + #i* NI /</td>
<td>23</td>
<td>2112</td>
<td>0</td>
</tr>
<tr>
<td>[l]</td>
<td>END_MARKER</td>
<td>IN + NI + #i* NI /</td>
<td>24</td>
<td>2112</td>
<td>0</td>
</tr>
</tbody>
</table>

The space occupied by the operand substring $a_d, a_{d-1}, \ldots, a_b$, has to be considered as a stack pre-set with $a_d, a_{d-1}, \ldots, a_b$. Starting from the top element of the stack, $T.OPD(a_b)$, and the first operator in the connected operator substring ($b_1$), we generate the following assembly code:

1. LDA T.OPD
2. NEG
3. if $b_1$ is the unary operator (#), or
4. LDA BT.OPD
5. OPT T.OPD

If $b_1$ is a binary operator (+, -, *), where BT.OPD designates generally the below-the-top element of the stack, and OPT is a symbolic representation of operators (ADD, SUB, MUL, DIV, NEG).

If any of succeeding operators is unary, an NEG is generated. Otherwise, the operator is checked for the commutativity. If it is a commutative one, an instruction line

OPT T.OPD

is generated. The noncommutative operator requires the generation of the code

STA T

with

T.OPD ← 'T'

and then

LDA BT.OPD
OPT T.OPD

where $T$ is a temporary variable. The code generation is continued in this way until $N^0 = 1 \lor N^0 = 0$. Then, the next $CS_{i+1} = \text{true}$ is taken.

procedure code generator (EP, VG, IL, NCS);
comment: The procedure code generator generates the assembly code for arithmetic expressions starting from the right end of the postfix string with an associated vector-generatrice.
EP = postfix string,
VG = vector-generatrice,
IL = generated instruction lines,
NCS = number of elementary units,
$N_{O}$ = number of operators in the current elementary unit,
$N_{I}$ = number of operands in the current elementary unit,
$C_{S}$ = current elementary unit,
$C_{S_{MINUSONE}}$ = elementary unit right to the current one,
$C_{S_{MINUSONE}}$ = elementary unit left to the current one,
T = pointer to the beginning of a 3-tuple in VG,
T = pointer to the indexed 3-tuple currently handled,
OPT = symbolic representation of operators (ADD, SUB, MUL, DIV, NEG),
OPT = standard representation of operator (+, -, *, /, #) in the operator substring,
T.OPD = top element of the stack for $CS_{i}$,
T.OPD = bottom element of the stack for $CS_{i}$,
T = symbolic representation of temporary variable;

begin
if $T.OPD = (NCS - 1)*3 + 1$;
$N_{O} = VG[T]$;
$N_{I} = VG[T + 1]$;
if $N_{I} = 0$ then begin
generate_code_line LDA T.OPD; goto exit;
end
else if $N_{O} = 1$ then
L1: begind comment: terminate code generation from an elementary unit $C_{S}$ and access the next one $C_{S_{MINUSONE}}$;
$T.OPD = T.OPD_{and NEG}$; goto L5;
end
else if $N_{I} = 1$ then
L1: begin
generate_code_lines LDA T.OPD; goto exit;
end
end
In the resume of each step. When they are extracted from there, one obtains the following code:

(1) \[ VG_3 = 1 1 22 \]
\[ CS_3 = I + \]
\[ l_c = 19 \]
\[ NI_0 = 1 \]
\[ NI_1 = 1 \]
\[ l = 13 \]
\[ l_c = 16 \]

(2) \[ VG_4 = 1 2 19 \]
\[ CS_4 = I + \]
\[ l_c = 10 \]
\[ NI_0 = 2 \]
\[ NI_1 = 2 \]
\[ l = 10 \]
\[ l_c = 7 \]

(3) \[ VG_5 = 1 1 17 \]
\[ CS_5 = I + \]
\[ l = 13 \]
\[ NI_0 = 1 \]
\[ NI_1 = 1 \]
\[ l_c = 10 \]

(4) \[ VG_6 = 2 3 12 \]
\[ CS_6 = T / \]
\[ l = 7 \]
\[ l_c = 4 \]

(5) \[ VG_7 = 2 1 9 \]
\[ CS_7 = I + \]
\[ l = 7 \]
\[ NI_0 = 0 \]
\[ NI_1 = 1 \]
\[ l_c = 0 \]

(6) \[ VG_8 = 0 2 12 \]
\[ CS_8 = T / \]
\[ l = 8 \]
\[ l_c = 4 \]

It should be noted that the algorithm is presented in a descriptive form. Many irrelevant details are dropped in order to provide a clear and concise presentation of the basic idea underlying the algorithm. For example, there is only one temporary variable \((T)\) although there can be more. Furthermore, the variables \(NI_0\) and \(NI_1\) are introduced for convenience and readability, although they were not necessary. Each change of \(NI_0\) implies, in fact, the change of \(VG\) \([K]\), and each change of \(NI_1\) implies the change of \(VG\) \([K + 1]\) in an elementary unit pointed to by \(l_c\) or \(l\). The use of pointers \(l_c\) or \(l\) is not given in explicit form. Instead, their strategic use is described by comments which make an essential part of the algorithm description. The algorithm provides an automatic clearing of VG space with the exception of VG \([K + 2]\) component in each of its 3-tuples. This is of significance in the algorithm implementation.

As an example, we shall apply the algorithm to VG-based data structure whose generation is already described. Each step in the code generation corresponds to the current elementary unit. It includes the starting values of CS, the part of VG for that elementary unit (indicated by \(VG_i\) and \(CS_i\), \(i = 1, 2, 3, \ldots 8\)), and the results of operations performed until the current elementary unit is changed. Due to interdependence of elementary units, every item is denoted by a subscript to provide the identification of the elementary unit to whom an item belongs. The generated instruction lines are underlined...
Theorem. The binary tree and the postfix string with associated vector-generatrice give the code of the same length.

Proof. Let $CS_i$ be given by Eqn (10). It is evident that $CS_i$ can be represented generally as a partial subtree. The partial subtrees are those subtrees which have not all their nodes resolved. This incompleteness is the consequence of the fact that the relation

$$N_i^P = N_i^B + 1$$

is not always satisfied. Bearing in mind the procedure for the binary tree derivation, the graph representation of $CS_i$ must form a partial subtree in the binary tree structure. Thus, the code generated from $CS_i$ is of the same length as that obtained from the corresponding partial subtree belonging to the binary tree. If the following relation for $CS_i$

$$N_i^P \leq N_i^B$$

is valid, then, according to the given algorithm, $CS_{i-1}$ will be taken, causing an instruction line

STA T

being generated. When the code is generated from the binary tree, each node is examined in order to verify whether it contains a commutative operator. If it contains it, the left or right subtree of the node can be chosen. Otherwise, the right subtree must be chosen. Upon generation of the code from the chosen subtree, the alternative subtree is taken, with an instruction line

STA T

To finish the proof, we shall prove that for

$$N_i^P > N_i^B$$

one obtains the same length of code as with the binary tree moving to the right in VG space until $N_i^P = 1$ and $N_i^B = 0$. It is clear that when a $CS_i$ satisfying Eqn (13) is encountered, one or several partial subtrees satisfying Eqn (12) are already handled. These partial subtrees will complete the partial subtree satisfying Eqn (12) to obtain the resulting subtree satisfying Eqn (11). $CS_i$ which satisfies Eqn (13) corresponds to a left subtree in the binary tree. Thus, moving to the right in VG space will not cause any paired instructions STA and LDA to be generated due to transitions. Because we have to generate the code for a node having as its left side a partial subtree and as its right side the leaves, paired instructions STA and LDA

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will not be generated, with the exception of the surplus of operands in CS, which can be covered by non-commutative operators left over during the handling of previous CS, satisfying Eqn (12). Thus, the code generated from a CS satisfying Eqn (13) will be of the same length as the code generated from the corresponding partial left subtree and the leaves. This closes the proof.

In fact, the code generator based on the vector-generator simulates the code generation from the binary tree with only one exception that at each node the right subtree is firstly handled independently from the commutativity of operators. The consequence of that can be only the different generated code.

**EXPERIMENTAL RESULTS**

An experiment has been performed over a set of arithmetic expressions transformed to postfix strings, binary trees and postfix strings with associated vector-generatrices in order to examine the time performances of algorithms and the difference in length of the generated code. The set of arithmetic expressions is divided in subsets each comprising arithmetic expressions of the same length. The initial length was 6 and the final one 72. The length is varied in increments of 6. The end marker is accounted for in the length. Each subset contained 15 expressions. Furthermore, two additional subsets are taken into consideration: (1) a subset consisting of a single operand (i.e., \(A\)) and (2) a subset consisting of two operands (i.e., \(A + B\), \(A * B\), etc). In order to achieve a uniform distribution in length space, these subsets are filled to also contain 15 expressions (length 2 and 4). The time performances are obtained by evaluating the mean values for each subset. Two classes of comparisons are performed: (1) postfix string–binary tree, and (2) binary tree–postfix string with associated vector-generator.

Firstly, the performances of postfix string and binary tree are compared. The comparisons are based on parameters \(D_L\), \(D_L^t\), \(D_T\) and \(D_T^t\) defined in the following way:

\[
D_L = \frac{L_{ps} - L_{bt}}{L_{bt}} \quad (14) \tag{14}
\]

\[
D_L^t = \frac{\sum^n (T_{bt} - T_{ps})}{\sum^n T_{ps}} \quad (15) \tag{15}
\]

\[
D_T = \frac{\sum^n (T_{bt} + T_{ps})}{\sum^n T_{ps}} \quad (16) \tag{16}
\]

\[
D_T^t = \frac{\sum^n (T_{bt} + T_{ps}) - \sum^n (T_{ps} + T_{bt})}{\sum^n T_{ps}} \quad (17) \tag{17}
\]

where \(m\) = number of subsets,
\(L_{ps}\) = length of code generated from postfix string,
\(L_{bt}\) = length of code generated from binary tree,
\(D_L\) = relative difference in length of generated code,
\(T_{ps}\) = mean time spent to syntax analysis and transformation to postfix string,
\(T_{bt}\) = mean time spent to syntax analysis and transformation to binary tree,
\(D_L^t\) = relative difference in time spent to syntax analysis and transformation,
\(T_{ps}^c\) = mean time spent to code generation from postfix string,
\(T_{bt}^c\) = mean time spent to code generation from binary tree,
\(D_T^t\) = relative difference in time spent to code generation,
\(D_T\) = total relative difference in time spent to code generation, syntax analysis and transformation.

It was found that \(D_L\) was between about 0–0.4. This fact becomes significant in highly repetitive loops with a large number of calculations. The code obtained from the binary tree is shorter because the whole arithmetic expression is in a form which provides the alternatives in particular points during the code generation. There are no such alternatives in the postfix string, which is scanned strictly sequentially, thus eliminating any possibility of economizing in the code generation.

The results obtained for \(D_L^t\), \(D_T^t\) and \(D_T\) (0.15, 0.33 and 0.24 respectively) show that these parameters are practically independent for expression length, in a statistical sense, when evaluated separately for each subset \((m = 1)\). The memory requirement for transformation to the binary tree was 50% higher and approximately the same for the phase of the code generation.

The second class of comparisons made between the binary tree and postfix string with the associated vector-generator represents the key point in the experiment. The results are presented in Table 2.

<table>
<thead>
<tr>
<th>(D_L)</th>
<th>(D_L^t)</th>
<th>(D_T)</th>
<th>(D_T^t)</th>
<th>(D_L)</th>
<th>(D_L^t)</th>
<th>(D_T)</th>
<th>(D_T^t)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0.03</td>
<td>0.21</td>
<td>0.13</td>
<td>0.12</td>
<td>0.12</td>
<td>0.16</td>
<td>0.024</td>
</tr>
</tbody>
</table>

The results of the comparison of binary tree to postfix string with vector-generator are defined analogously to Eqns (14), (15), (16) and (17):

\[
D_L = \frac{L_{bt} - L_{ps}}{L_{bt}} \quad (18) \tag{18}
\]

\[
D_L^t = \frac{\sum^n (T_{bt} - T_{ps})}{\sum^n T_{bt}} \quad (19) \tag{19}
\]

\[
D_T = \frac{\sum^n (T_{bt} + T_{ps})}{\sum^n T_{bt}} \quad (20) \tag{20}
\]

\[
D_T^t = \frac{\sum^n (T_{bt} + T_{ps}) - \sum^n (T_{ps} + T_{bt})}{\sum^n T_{ps}} \quad (21) \tag{21}
\]

It was found also that the parameters \(D_T\), \(D_T^t\) and \(D_T\) were independent on expressions length \((m = 1)\). \(D_L^t, D_T^t, D_T\) and \(D_T^t\) are defined as follows:

\[
D_L^t = \frac{M_{bt} - M_{ps}}{M_{bt}} \quad (22) \tag{22}
\]

\[
D_T = \frac{M_{bt}^c - M_{ps}^c}{M_{bt}^c} \quad (23) \tag{23}
\]

\[
D_T^t = \frac{(M_{bt}^c + M_{ps}^c) - (M_{ps}^c + M_{bt}^c)}{M_{bt}^c + M_{ps}^c} \quad (24) \tag{24}
\]

\[
D_T^t = \frac{R_{bt} - R_{ps}}{R_{bt}} \quad (25) \tag{25}
\]
RIGHT-TO-LEFT CODE GENERATION FOR ARITHMETIC EXPRESSIONS

The machine output of the example includes the input postfix string, the modified output postfix string, the vector generatrice, the syntax analysis time ($T_1$), the code generation time ($T_2$), the total time $T (T_1 + T_2)$ and the generated code. The input string of the example is modified to include different, single character variables (not only $I$ and $N$),

$$A + B - (-(-B+C))(D*E-G/F)/M*N + G*S,$$

but the structure of the input string is left unchanged. The output is as follows:

**INFIX STRING:**

$A + B - (-(-B+C))(D*E-G/F)/M*N + G*S$

**POSTFIX STRING:**

$ABADD$BCADD # NEG # NEG/DE MULGFSDIVIVERSUBDIVMSDIV#VN MULSUBG ADDS$ vector generatrice is:

<p>| | | | | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>2</td>
<td>4</td>
<td>5</td>
<td>1</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>5</td>
<td>4</td>
<td>0</td>
</tr>
</tbody>
</table>

**SYNTAX ANALYSIS TIME:** $T_1 = 3.012$ MSEC

**GENERATED CODE IS:**

```
LDA G
DIV F
STA Z
LDA D
MUL E
SUB Z
STA Z
LDA B
ADD C
NEG NEG
DIV Z
DIV M
MUL N
STA Z
LDA A
ADD B
SUB Z
ADD G
```

**CODE GENERATION TIME:** $T_2 = 1.961$ MSEC

**TOTAL TIME:** $T = 4.973$ MSEC

Besides this general experiment, a subexperiment is performed over two subsets of expressions of length 2 and 4 (single and two operand subsets) with uniform distribution. The same benefit is observed in the domain of the code generation, but the overall time gain (when the syntax analysis is taken into account) is reduced to 8%.

**CONCLUSION**

The right-to-left generator has demonstrated better characteristics than the ones obtained with a binary tree. The length of the code generated from a vector-generatrice is equal to one obtained from binary tree. The compilation time for arithmetic expressions (syntax analysis + code generation) is appreciably shorter. This benefit is more significant for the stand-alone code generator. The total memory space (syntax analyser + code generator) was approximately the same. The memory space required by the syntax analyser, based on the vector-generatrice, was greater but it was balanced by the decrease of the memory requirement for the corresponding code generator. With respect to programming, the code generator based on the vector-generatrice
was programmed with a smaller number of instruction lines, but not significantly.
It would be interesting to extend the investigations on
the application of the vector-generatrice to all operator precedence grammars and especially to the code optimization.

REFERENCES