

Special Section on InterPACK 2017—Part 2

InterPACK is a premier international forum for exchange of state-of-the-art knowledge in research, development, manufacturing, and applications of micro-electronics packaging. It is the flagship conference of the ASME Electronic and Photonic Packaging Division (EPPD) founded in 1992 as an ASME–JSME joint biannual conference.

Rapid changes in the semiconductor landscape together with findings from InterPACK Pathfinding workshop (IPW) in 2016 led to a significant reset of InterPACK conference priorities and focus to comprehensively address needs of the InterPACK community. As a result, starting in 2017, InterPACK has become an annual conference, and the scope of the conference has increased significantly together with a systems-focus to include some of the most cutting-edge topics in electronics packaging, device integration, and reliability. These topics are organized across five different tracks: (1) heterogeneous integration—microsystems with diverse functionality, (2) servers of the future, (3) structural and physical health monitoring, (4) energy conversion and storage, and (5) transportation: autonomous and electric vehicles.

InterPACK2017 had nearly 100 technical papers, 100 technical presentations, and about 50 keynotes and invited talks by thought leaders from industry, national labs, and academia. Based on peer-reviews of these conference papers, authors of a few select exemplary papers across all five tracks of the conference were invited to submit papers for the special sections of JEP. These papers were then subject to an independent peer review process in accordance with the editorial procedures of *ASME Journal of Electronic Packaging* (JEP). Part 2 of the InterPACK 2017 papers completes the June 2018 Special Section issue. Broadly, these

papers cover topics across all the five tracks of the conference and include papers on advanced interconnect technologies, 3D-printing, microscale heat transfer, data center cooling technologies, sensors for health monitoring, and reliability challenges in automotive electronics.

Again, this collection of papers will be an excellent resource for readers and subscribers of the *ASME Journal of Electronic Packaging*. We gratefully acknowledge all the authors whose work appears in these Special Section issues, and the reviewers help in improving the quality of these papers. We would also like to acknowledge InterPACK 2017 General Conference Chair Dr. Mehdi Asheghi and JEP Editor Professor Y. C. Lee for their support and cooperation.

Kaushik Mysore
Advanced Micro Devices, Inc.,
Austin, TX 78745

Sreekant Narumanchi
National Renewable Energy Laboratory (NREL),
Golden, CO 80401

Ercan Dede
Toyota Research Institute of North America,
Ann Arbor, MI 48105

Reza Khiabani
Google,
Mountain View, CA 94043

The United States Government retains, and by accepting the article for publication, the publisher acknowledges that the United States Government retains, a nonexclusive, paid-up, irrevocable, worldwide license to publish or reproduce the published form of this work, or allow others to do so, for United States Government purposes.